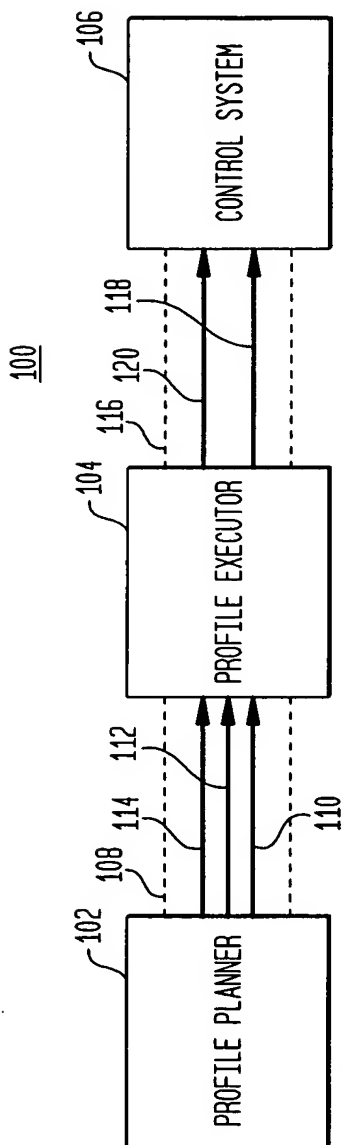


Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
 Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
 Title: Method and System for Efficient and Accurate
 Processing of a Discrete Time Input Signal

FIG. 1



Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
 Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
 Title: Method and System for Efficient and Accurate
 Processing of a Discrete Time Input Signal

FIG. 2

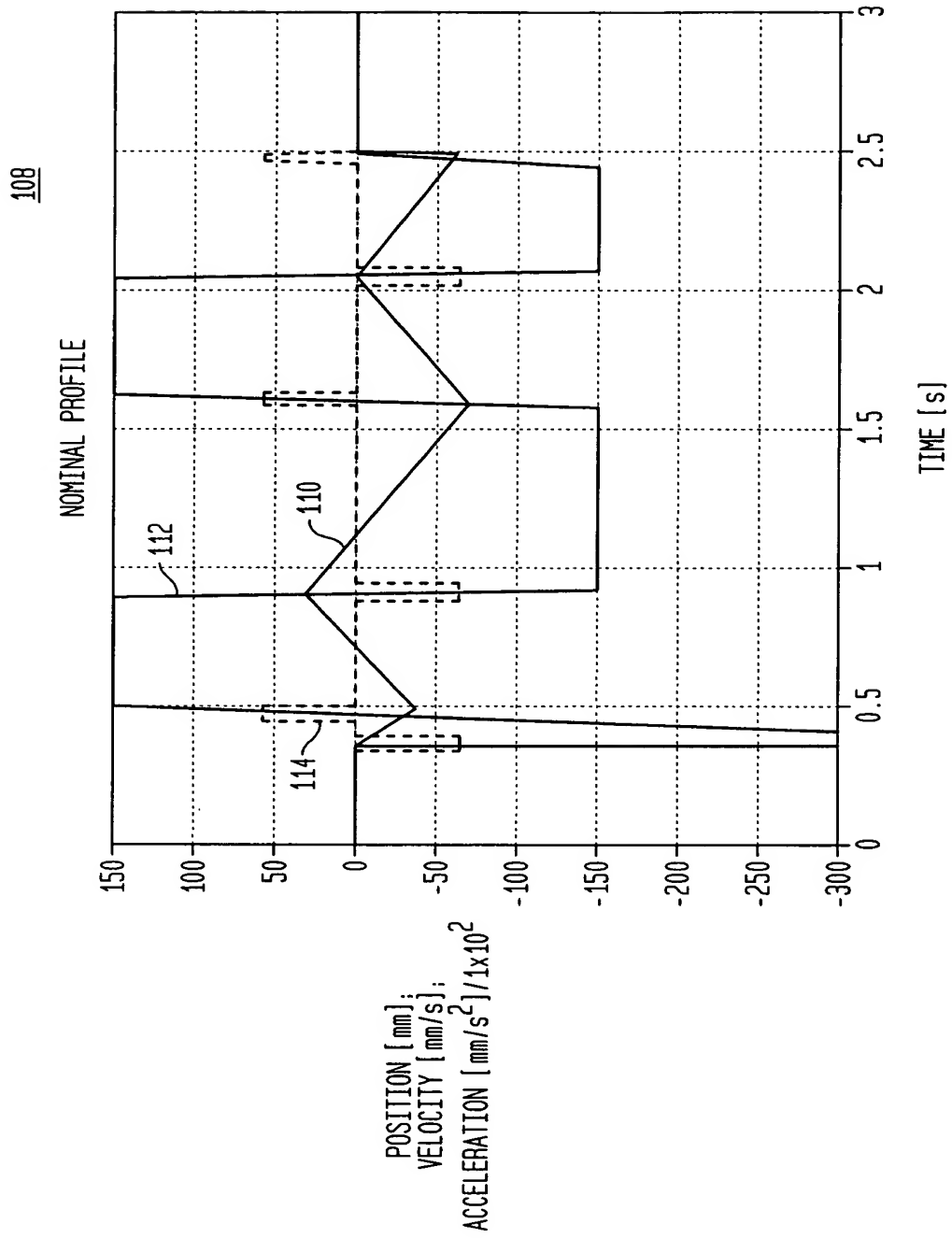


FIG. 3

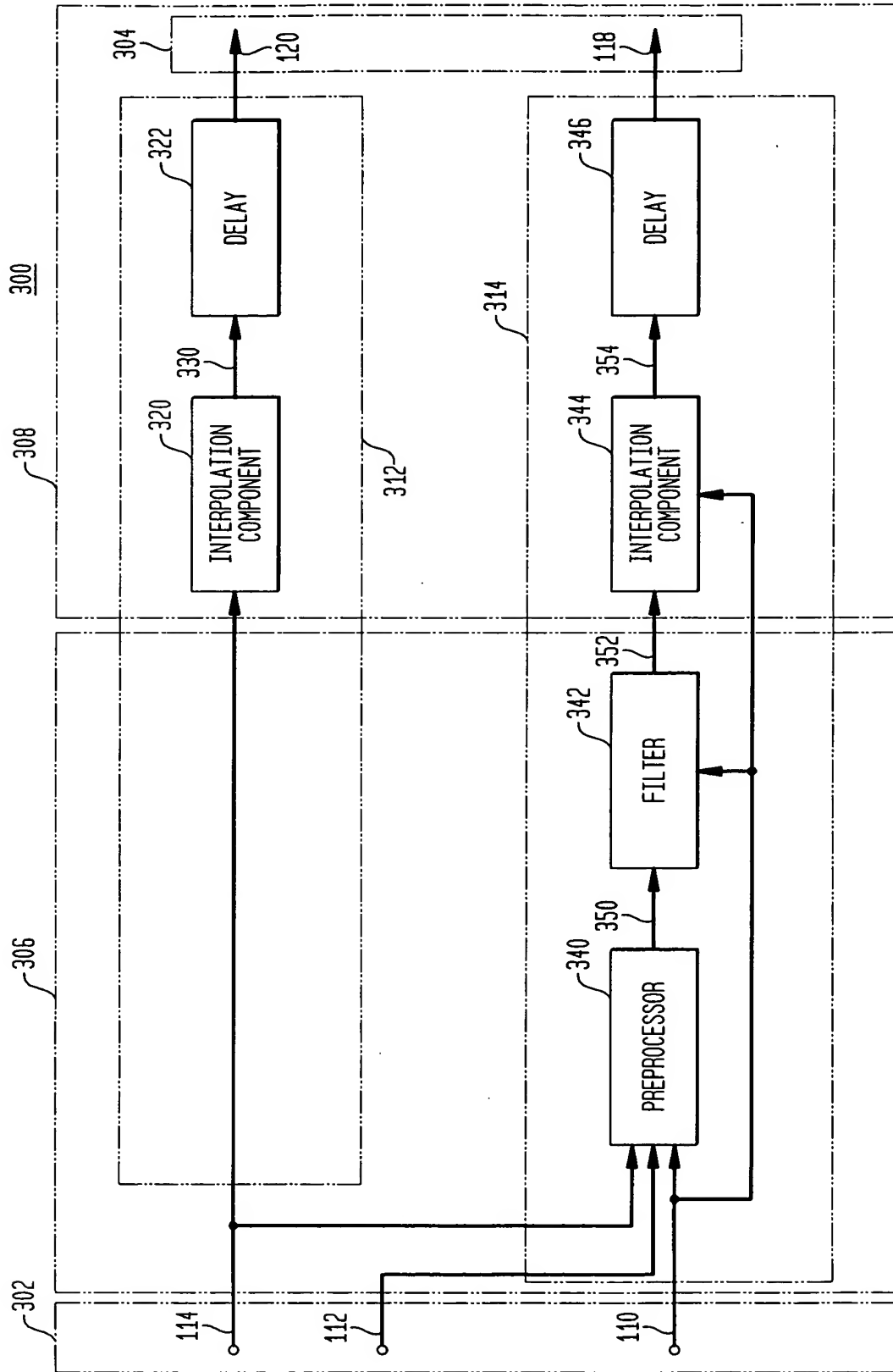


FIG. 4

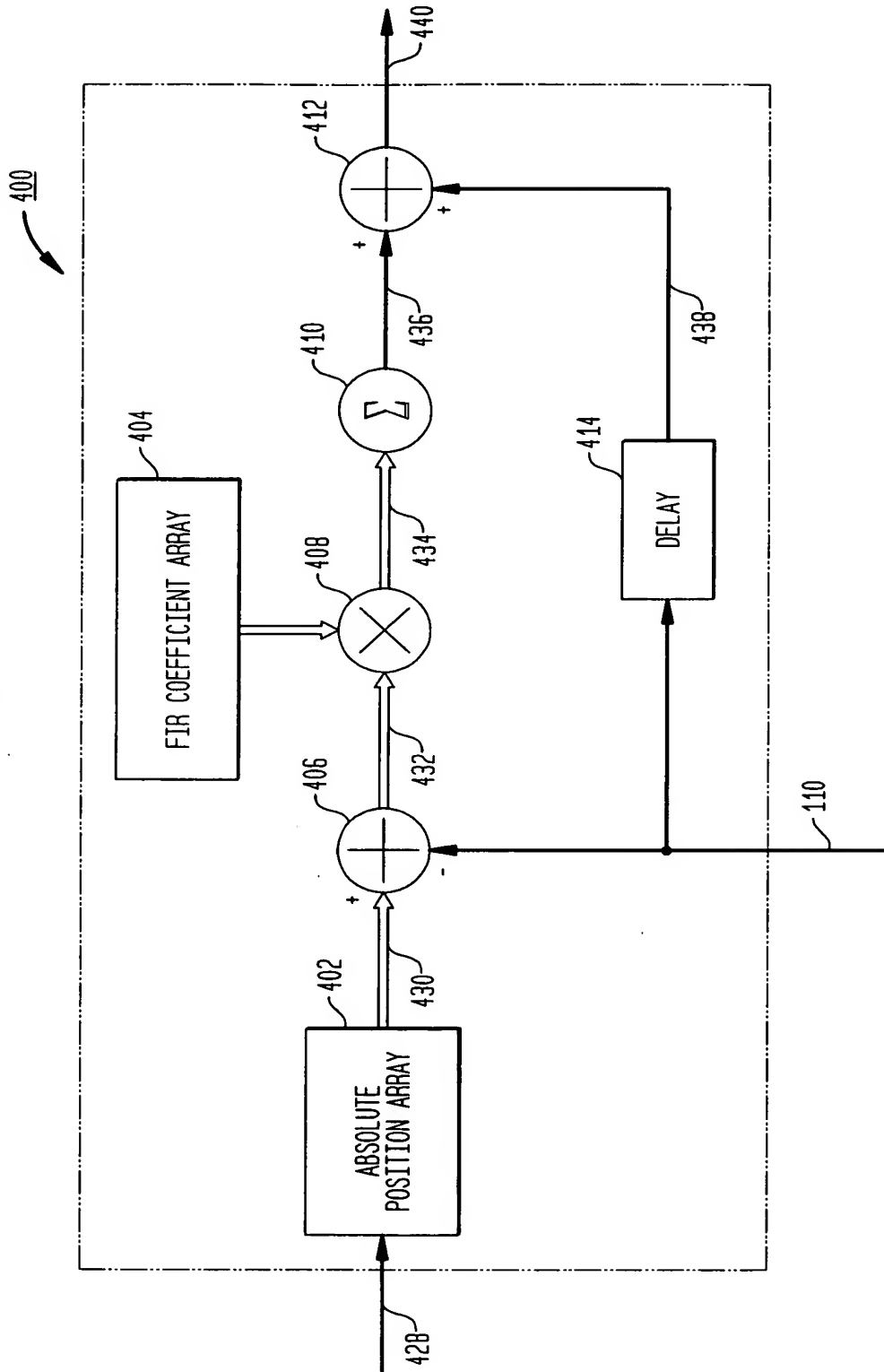
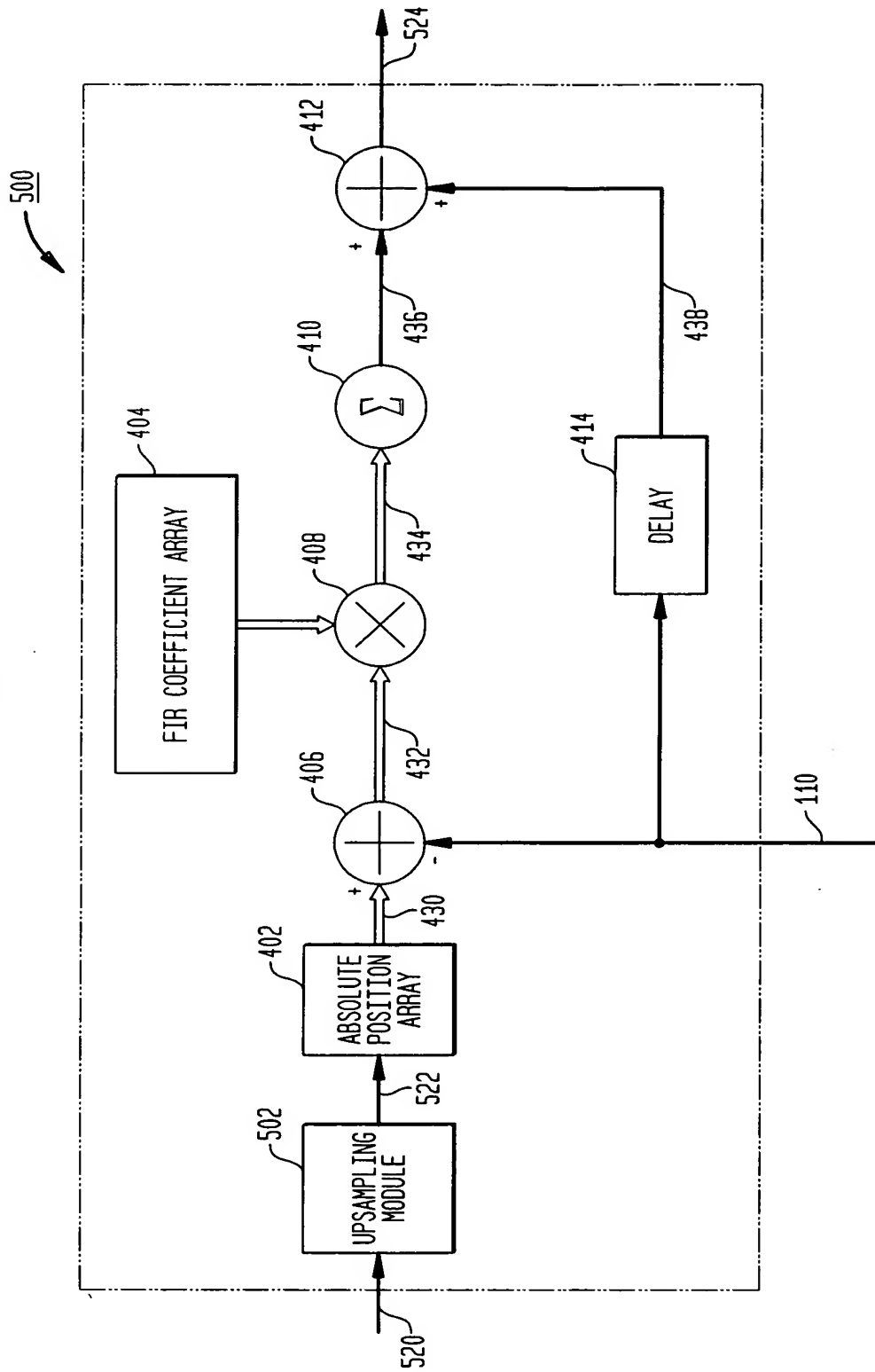
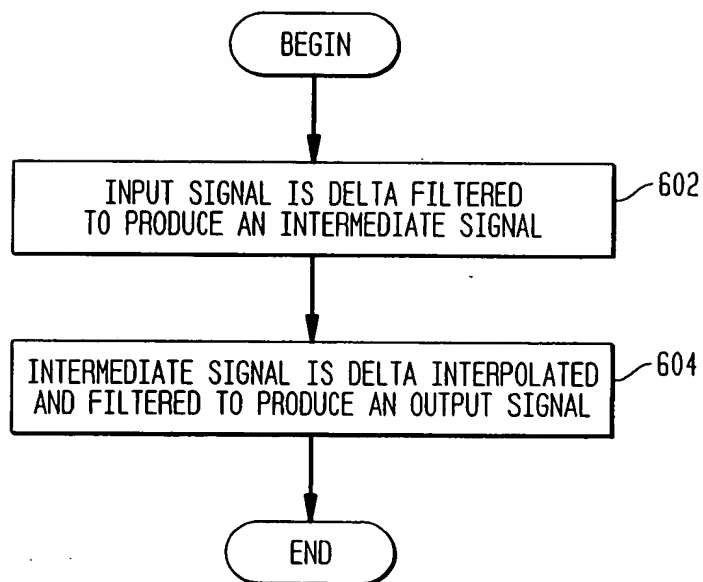


FIG. 5



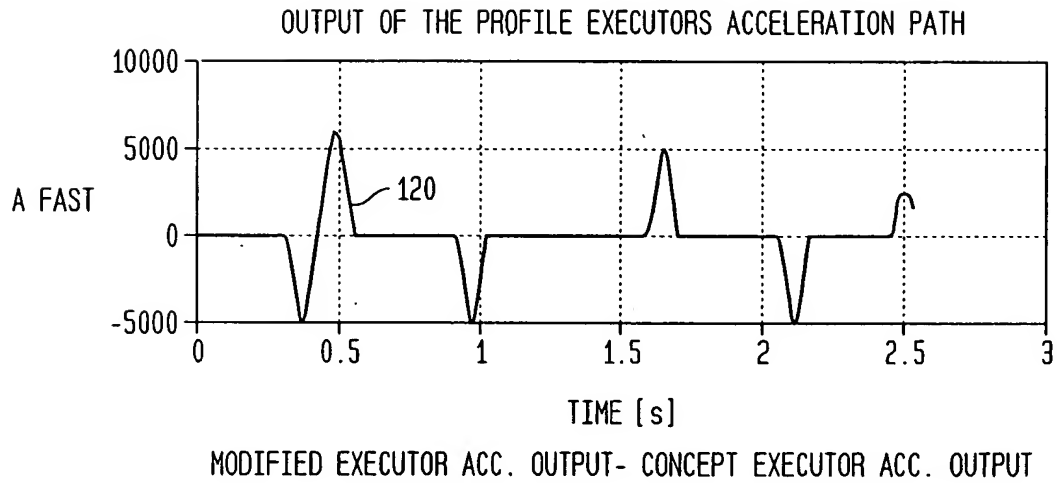
Appl. No. *To Be Assigned*; Filed: *Herewith*
Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
Title: Method and System for Efficient and Accurate
Processing of a Discrete Time Input Signal

FIG. 6



Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
 Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
 Title: Method and System for Efficient and Accurate
 Processing of a Discrete Time Input Signal

FIG. 7



Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
 Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
 Title: Method and System for Efficient and Accurate
 Processing of a Discrete Time Input Signal

FIG. 8

INTERPOLATION FILTERS FREQUENCY COMPARISON

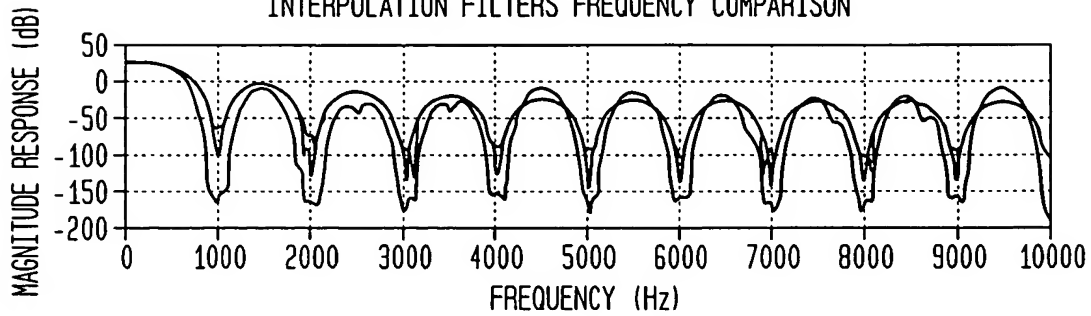


FIG. 9

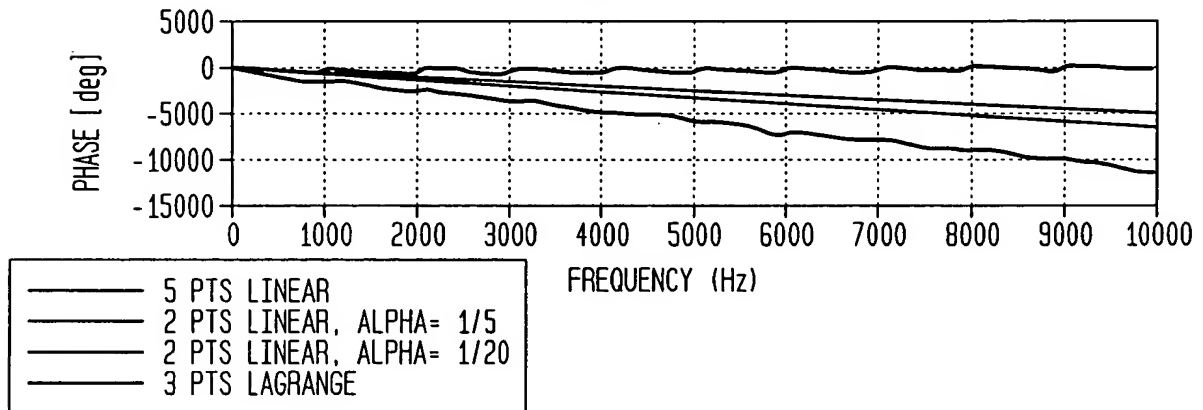


FIG. 10

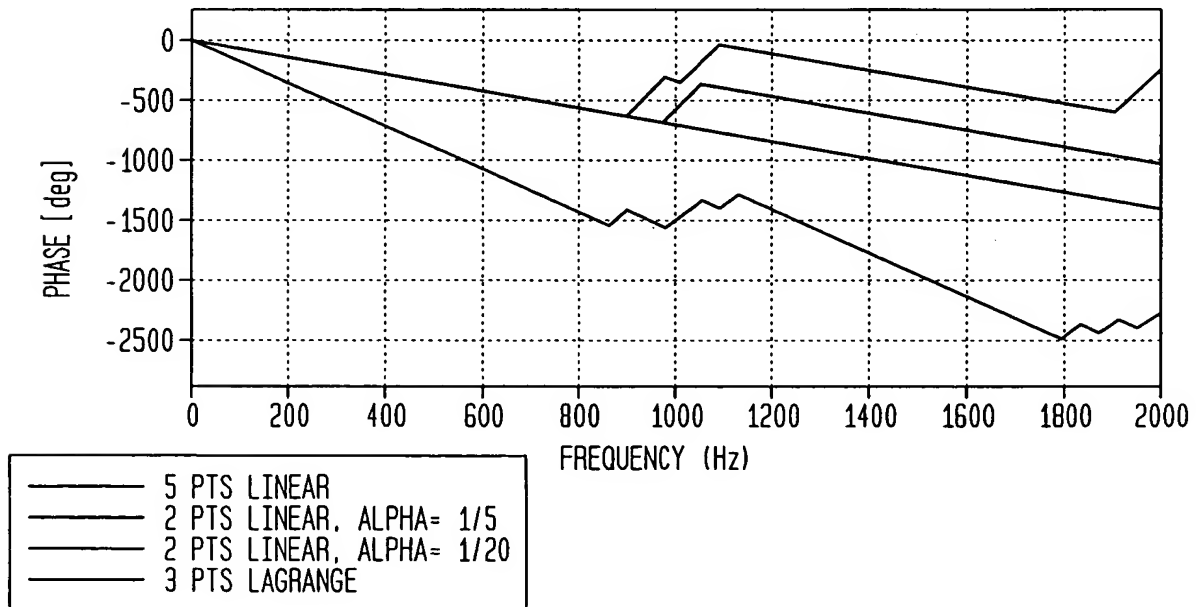
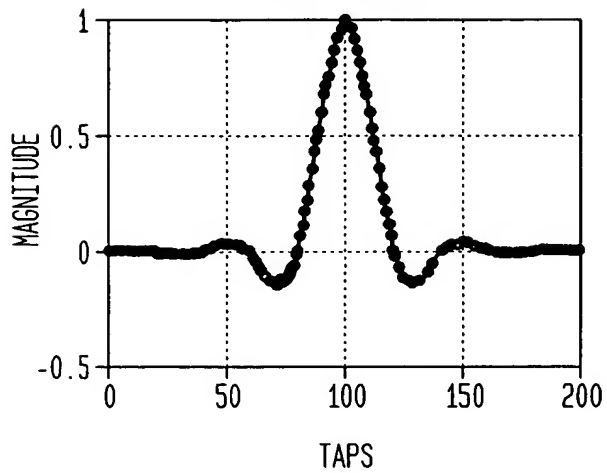
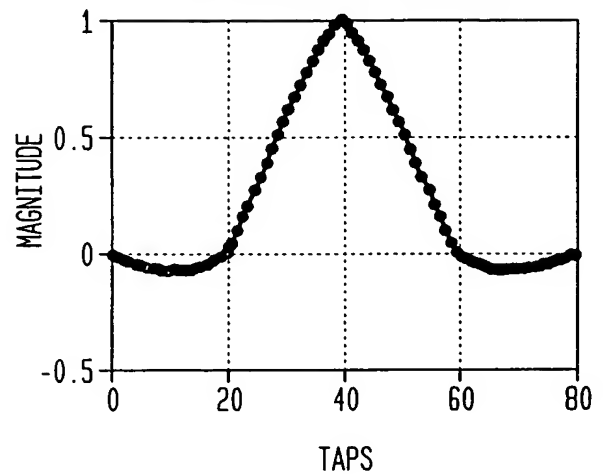


FIG. 11

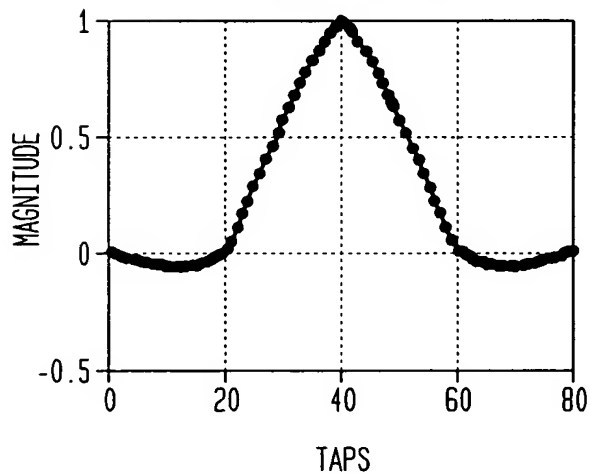
5 PTS LINEAR

**FIG. 12**

2 PTS LINEAR, ALPHA=1/5

**FIG. 13**

2 PTS LINEAR, ALPHA=1/20

**FIG. 14**

3 PTS LAGRANGE

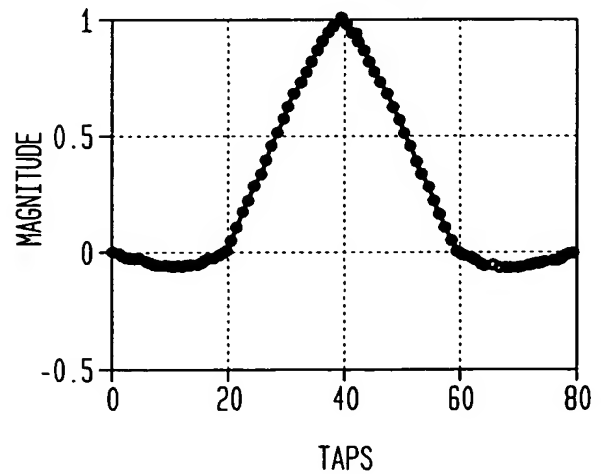


FIG. 15

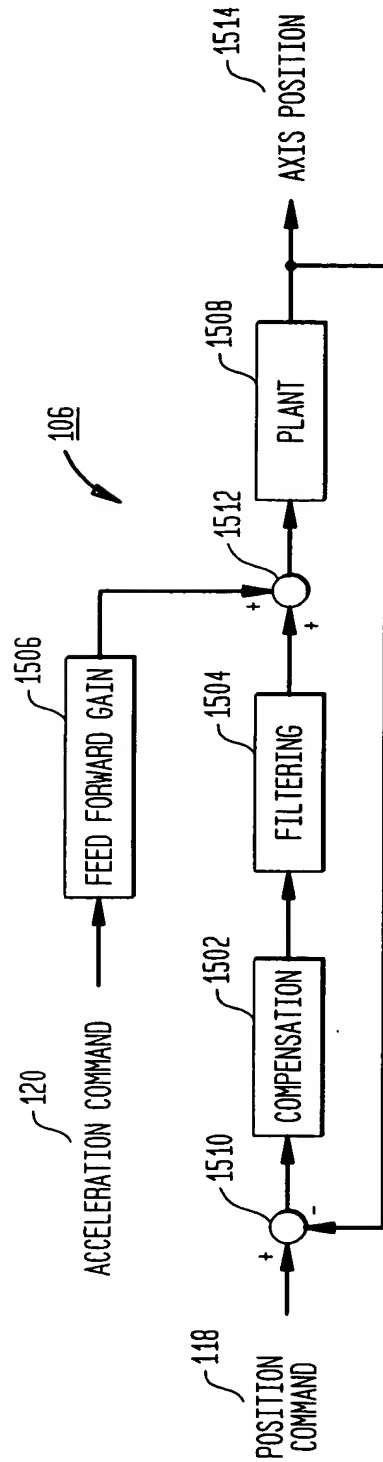


FIG. 16A

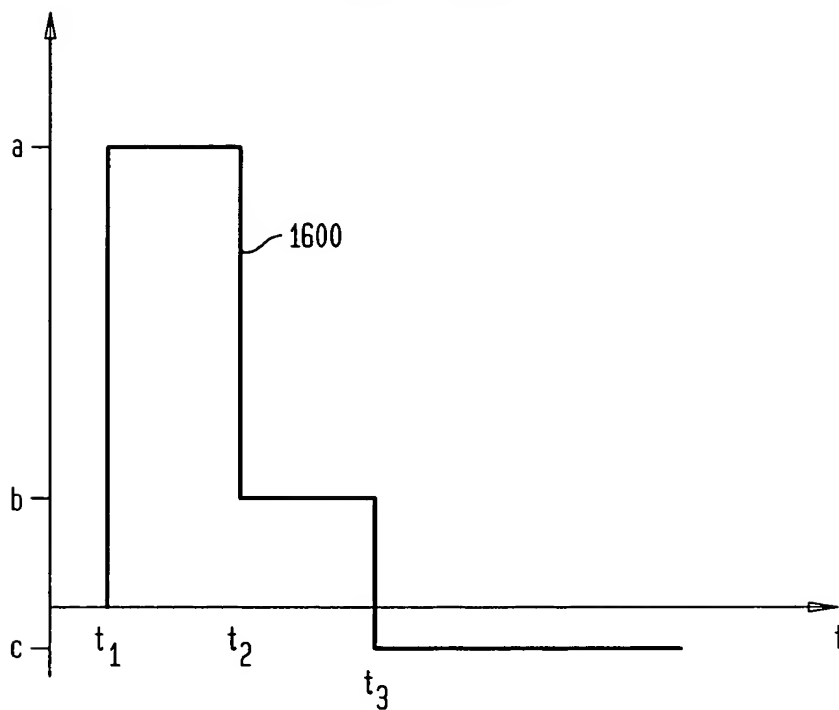
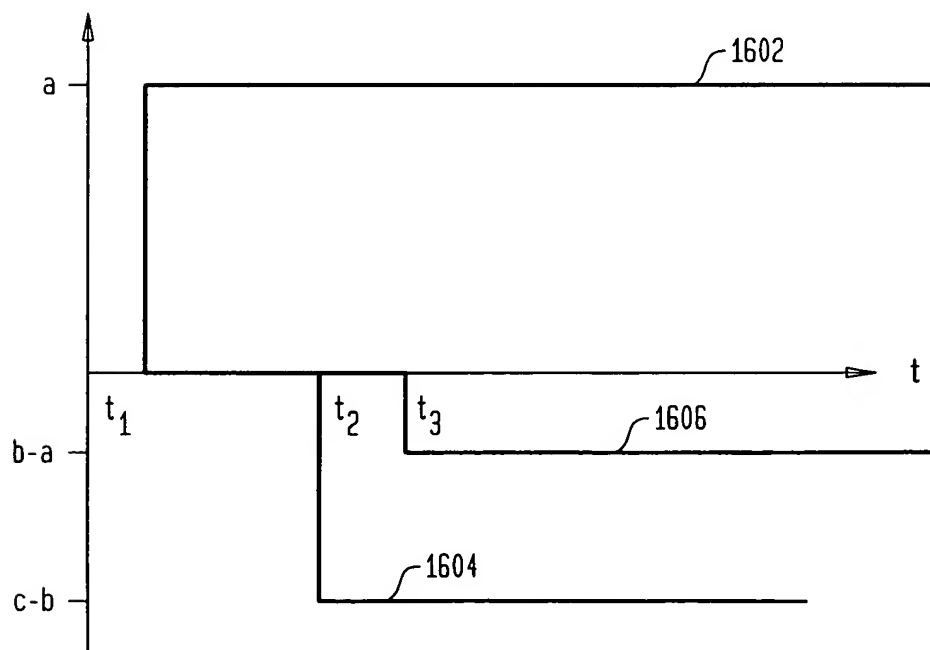
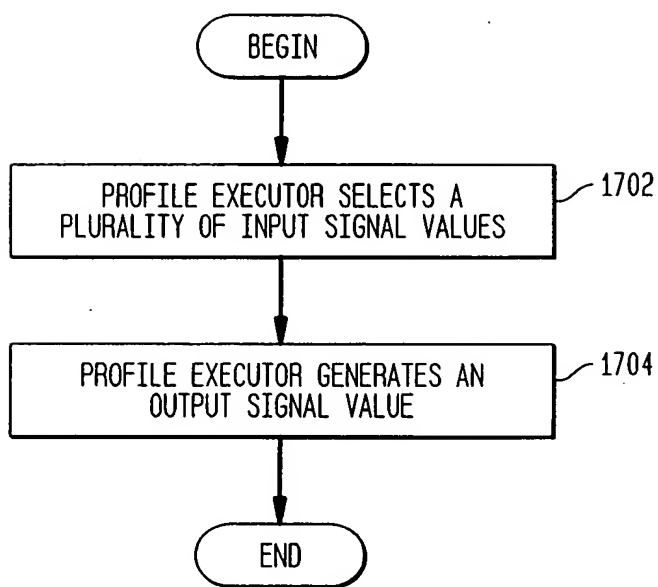


FIG. 16B

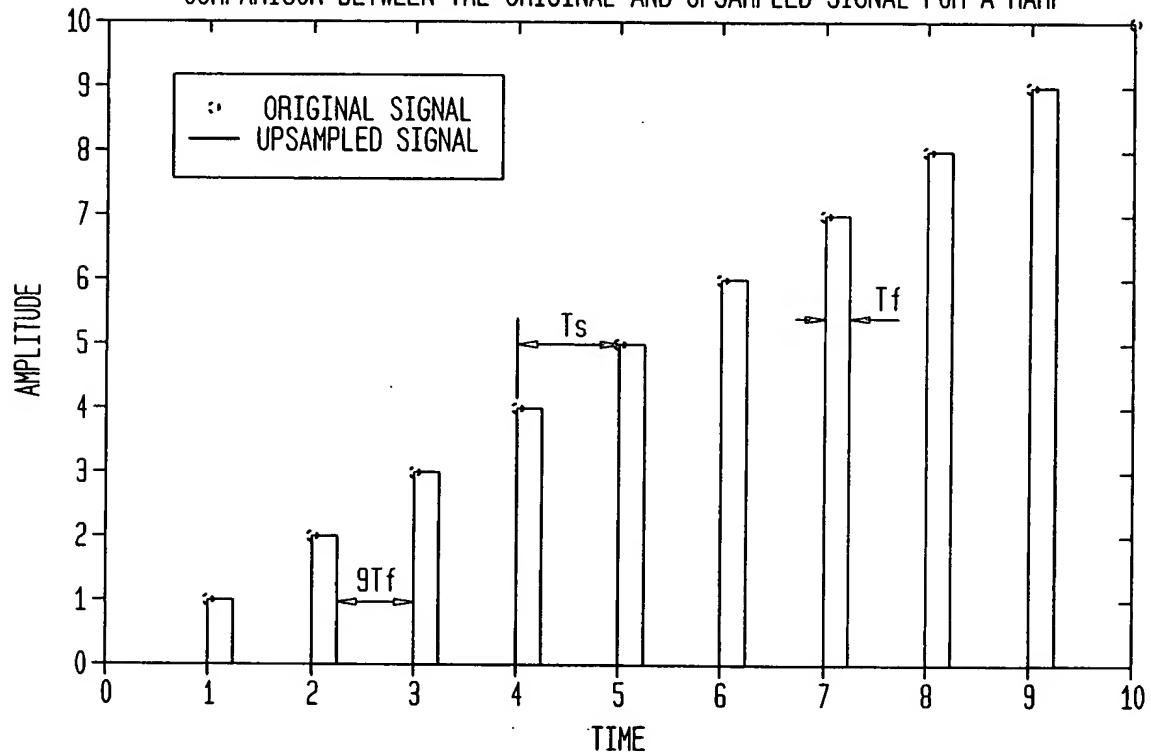


Appl. No. *To Be Assigned*; Filed: *Herewith*
Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
Title: Method and System for Efficient and Accurate
Processing of a Discrete Time Input Signal

FIG. 17



COMPARISON BETWEEN THE ORIGINAL AND UPSAMPLED SIGNAL FOR A RAMP



Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1857.0330002; Group Art Unit: *To Be Assigned*
 Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
 Title: Method and System for Efficient and Accurate
 Processing of a Discrete Time Input Signal

FIG. 19

